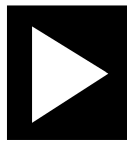
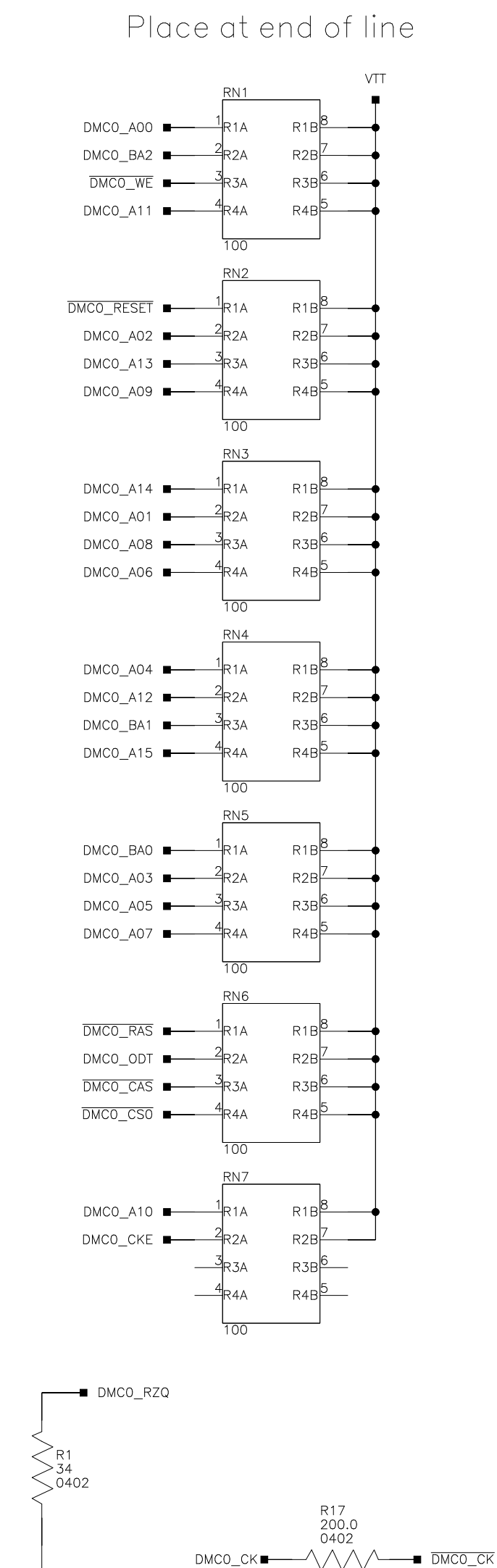
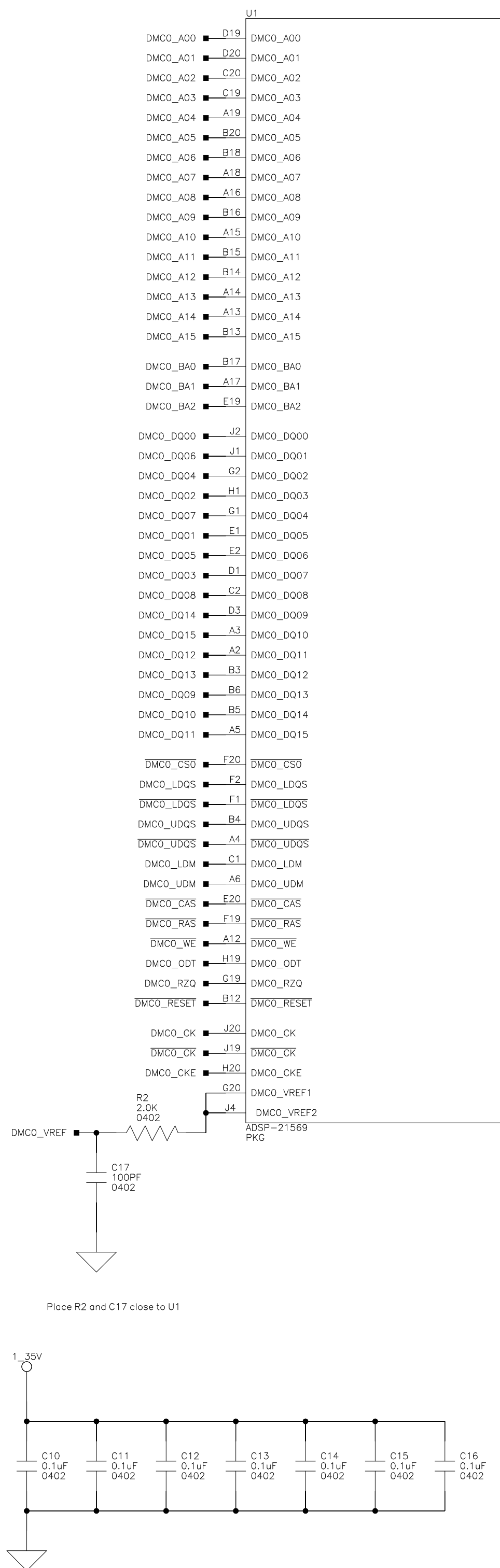


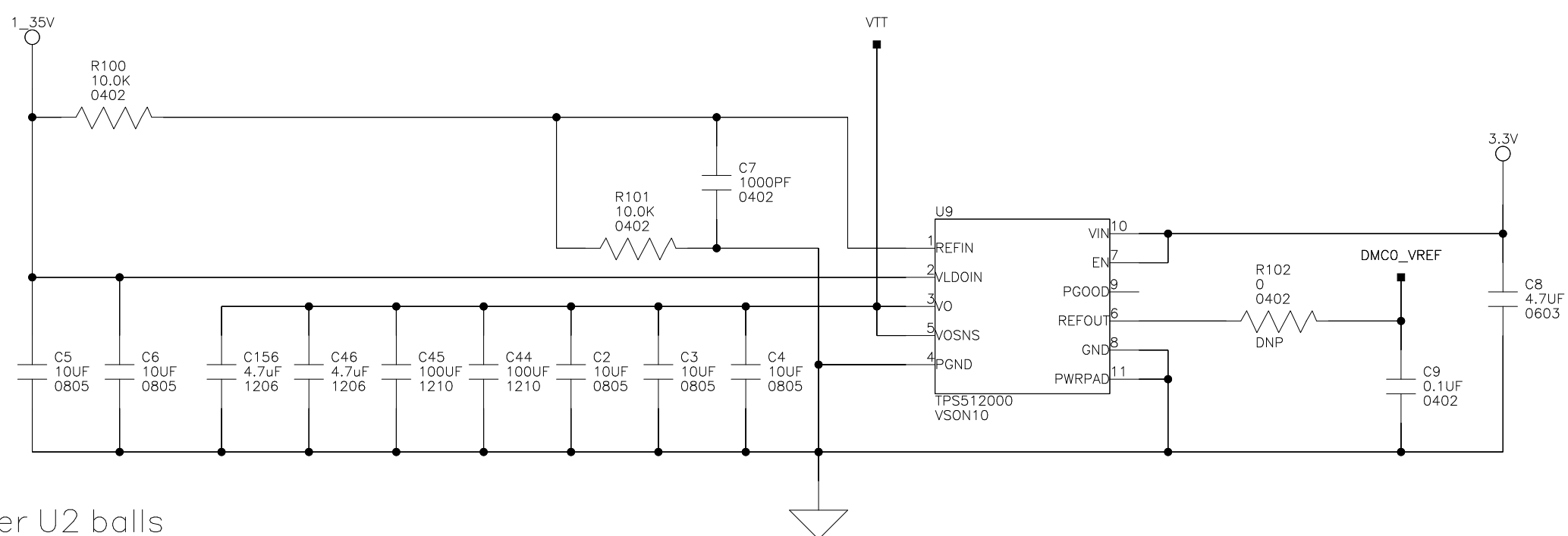
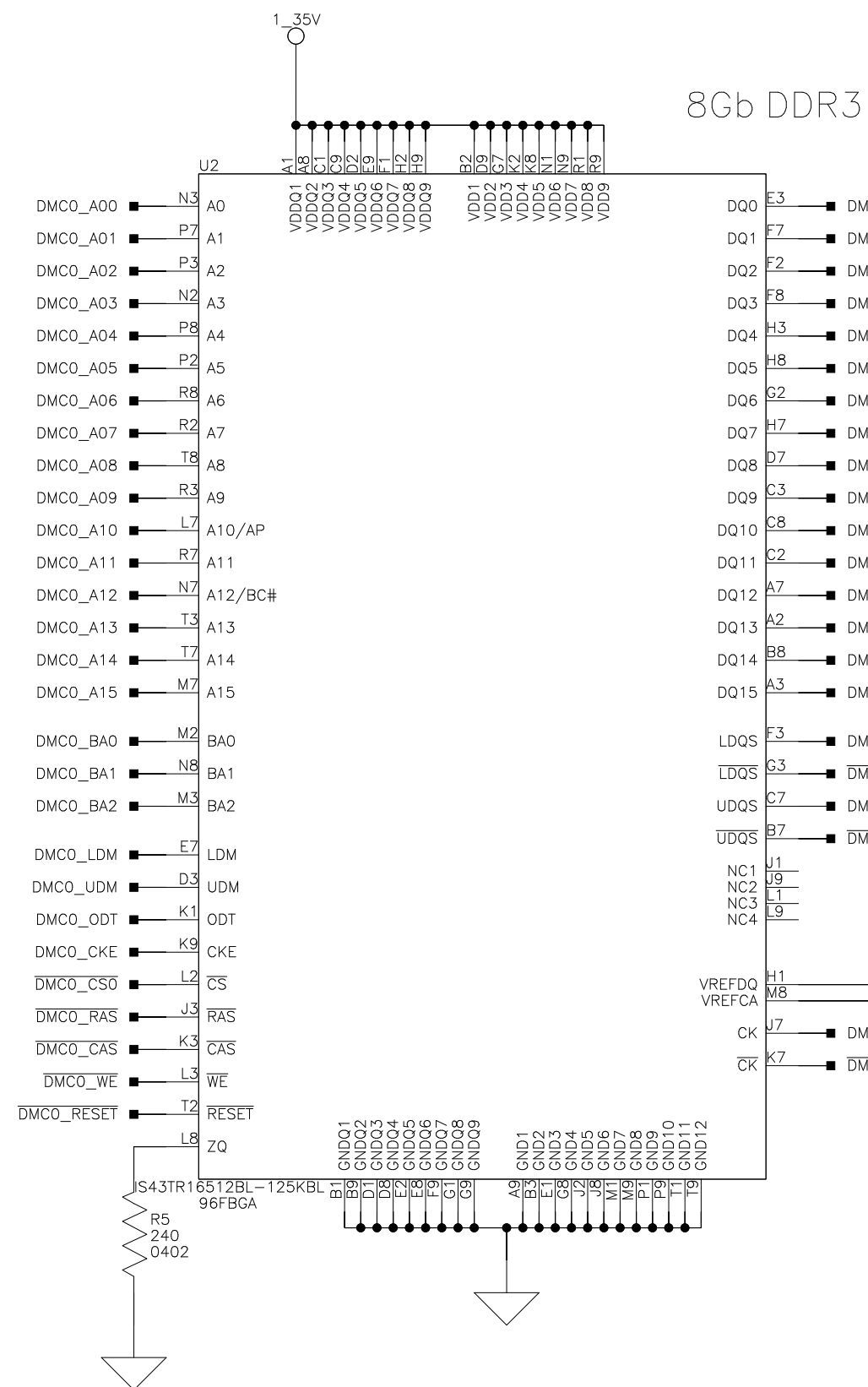
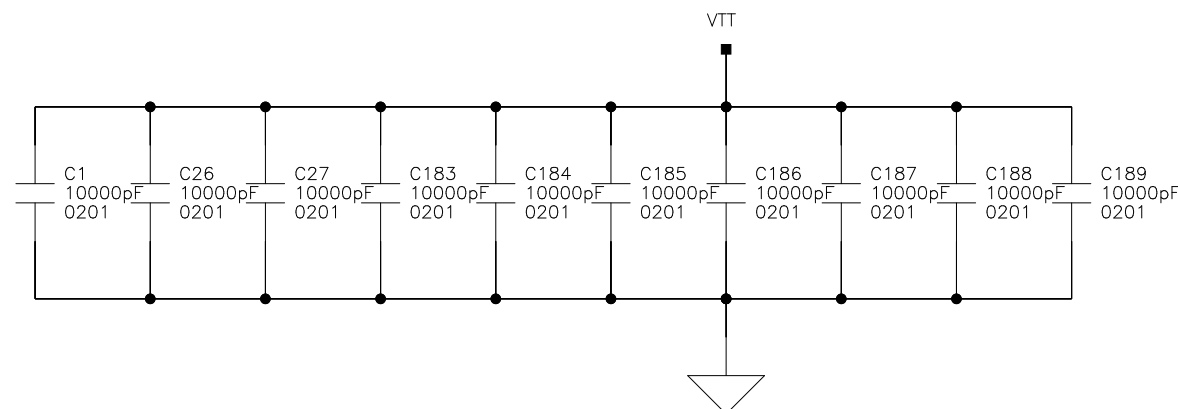
ADSP-21569 System on Module

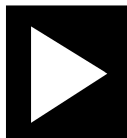
EV-21569-SOM
Schematic

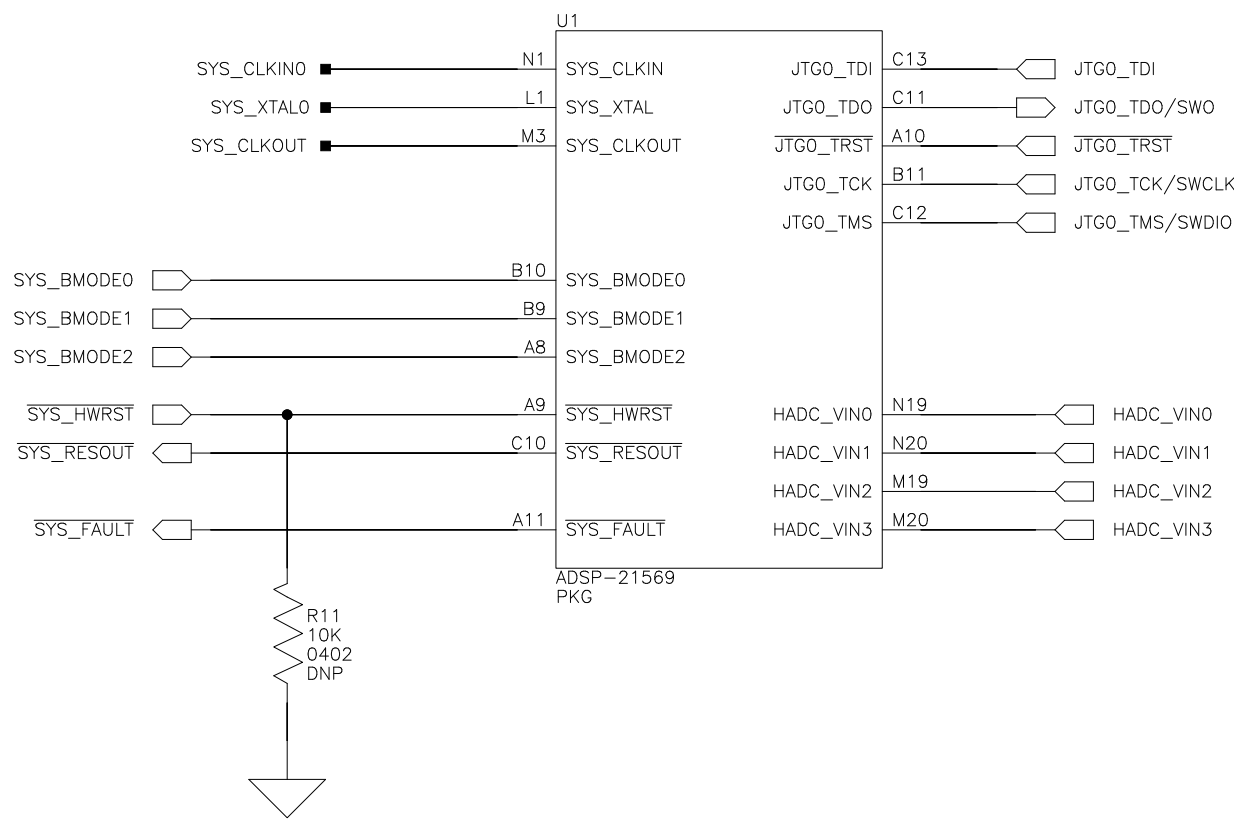
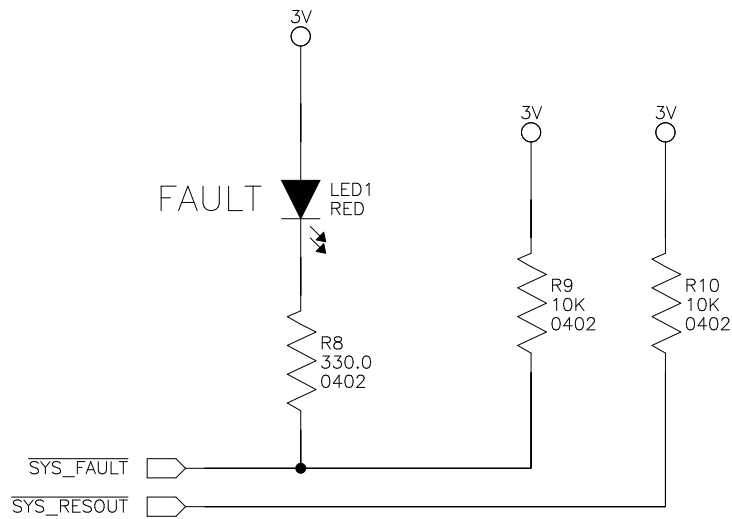
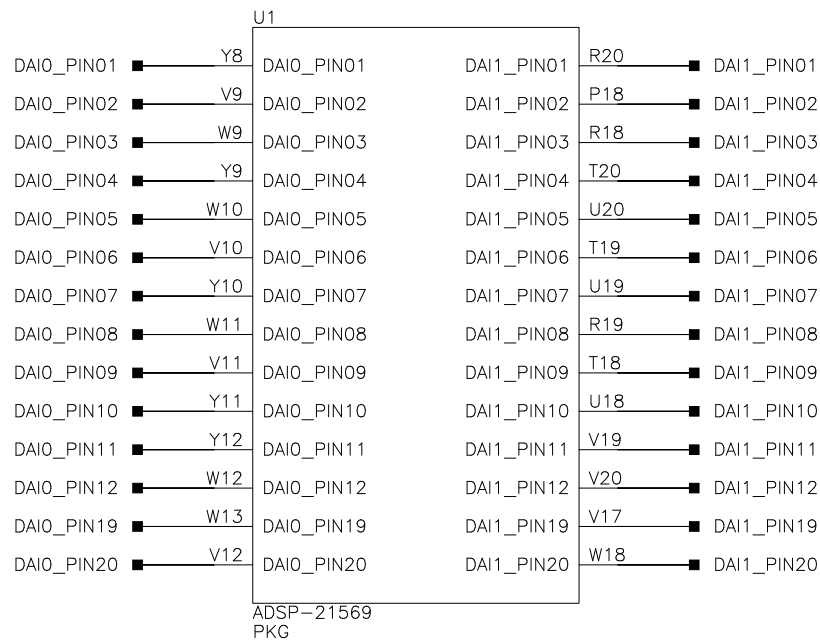
		ANALOG DEVICES		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title ADSP-21569 SoM Title Block					
Size C	Board No. EV-21569-SOM			Rev B	
Date 11/23/2020		Sheet 1 of 12			




Place inbetween U2 and U1 or under U2 balls



		ANALOG DEVICES		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title ADSP-21569 SoM DDR3 Interface					
Size C	Board No. EV-21569-SOM				Rev B
Date	11/23/2020			Sheet	2 of 12



		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title ADSP-21569 SoM Non-Mux Ports			
Size C	Board No. EV-21569-SOM		Rev B
Date	11/23/2020		Sheet 3 of 12

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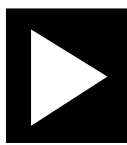
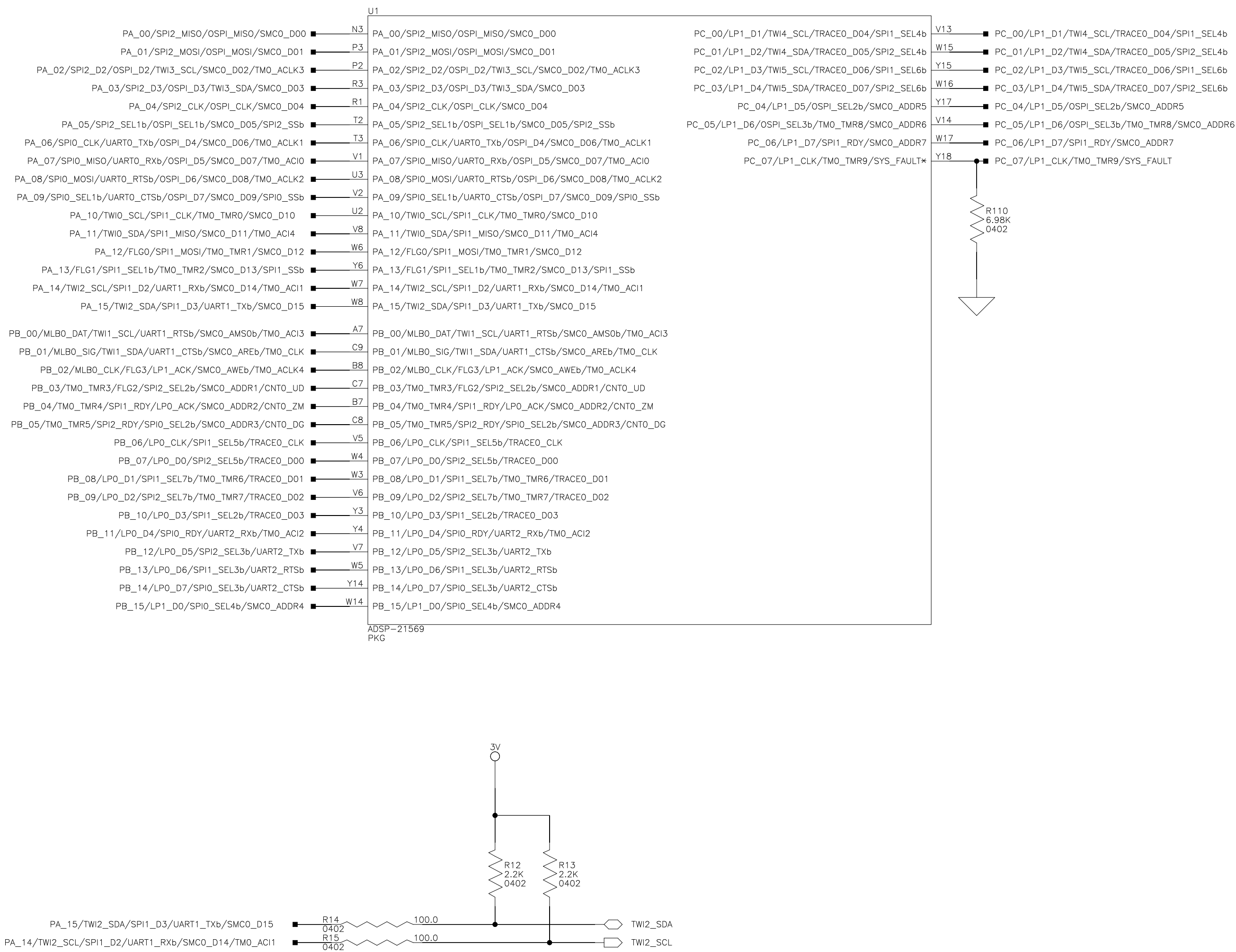
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**ANALOG
DEVICES**

2 Elizabeth Drive
Chelmsford, MA 01824
PH: 1-800-ANALOGD

Title			ADSP-21569 SoM Mux Ports		
Size C	Board No. EV-21569-SOM				Rev B
Date	11/23/2020		Sheet	4 of 12	

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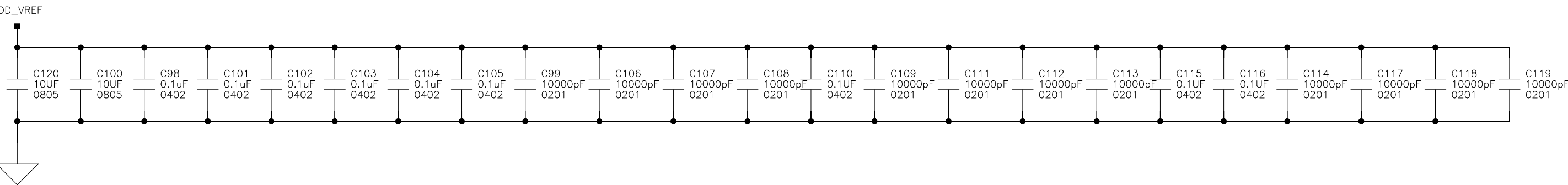
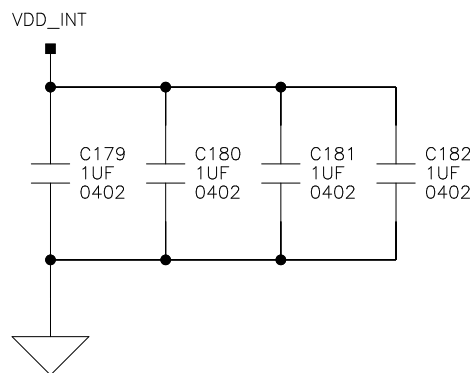
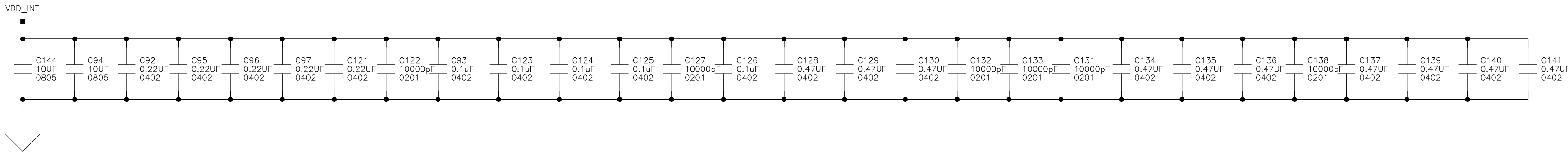
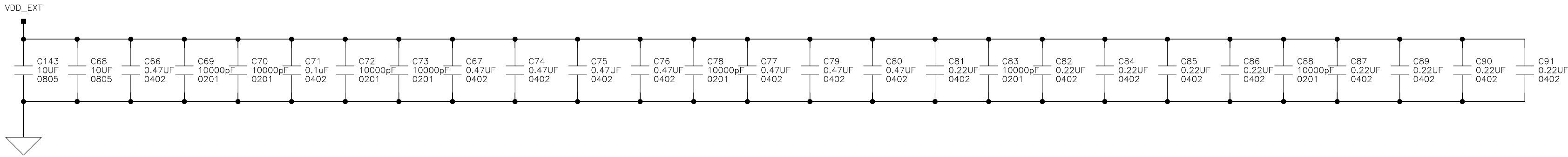
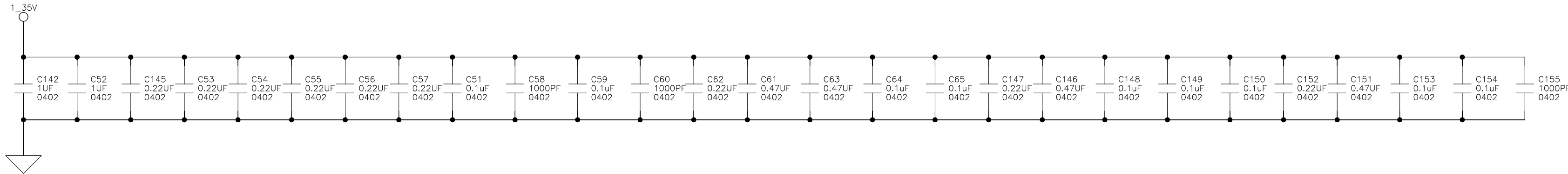
1

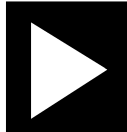
1

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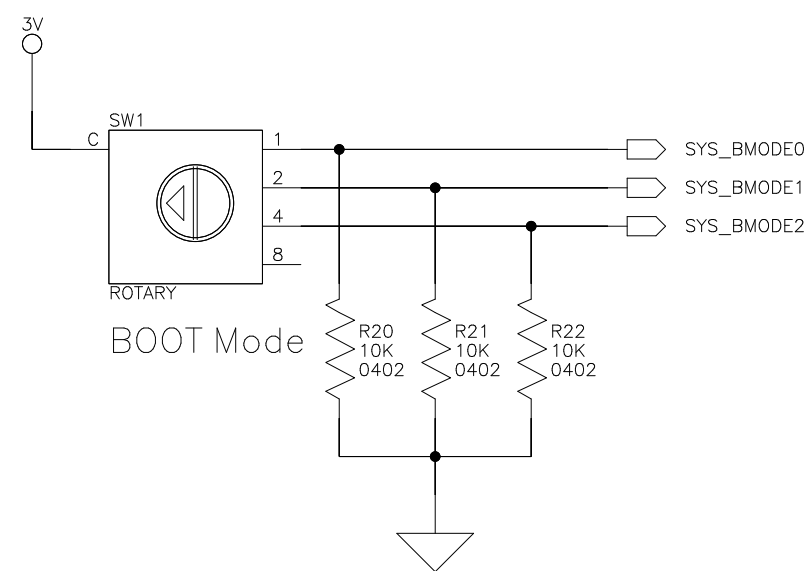
4



		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title ADSP-21569 SoM Proc Bypass Caps			
Size C	Board No. EV-21569-SOM		Rev B
Date	11/23/2020	Sheet 6 of 12	

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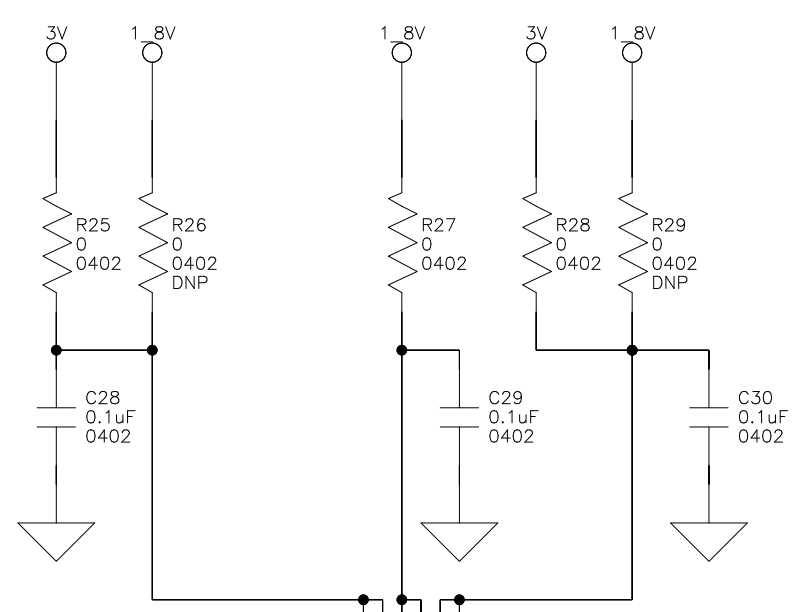
SW1: Boot mode selection switch

BMODE[2:0]	Boot Source
000	No boot/Custom ROM
001	SPI2 master boot
010	SPI2 slave boot
011	UART0 slave boot
100	LP0 slave boot
101	OSPI Boot
110	Reserved
111	Reserved

DEFAULT

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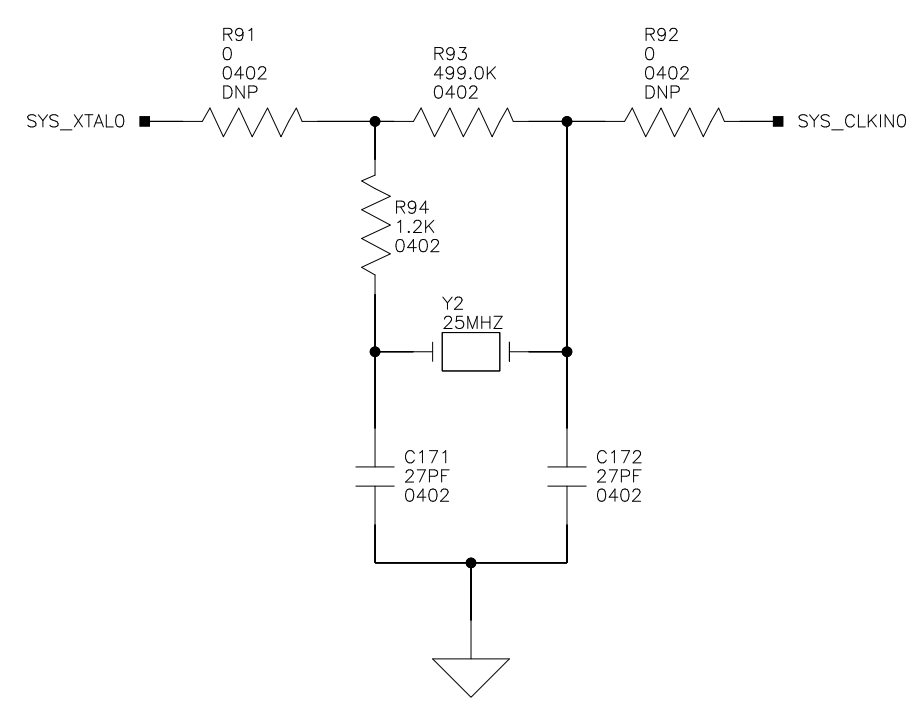


3

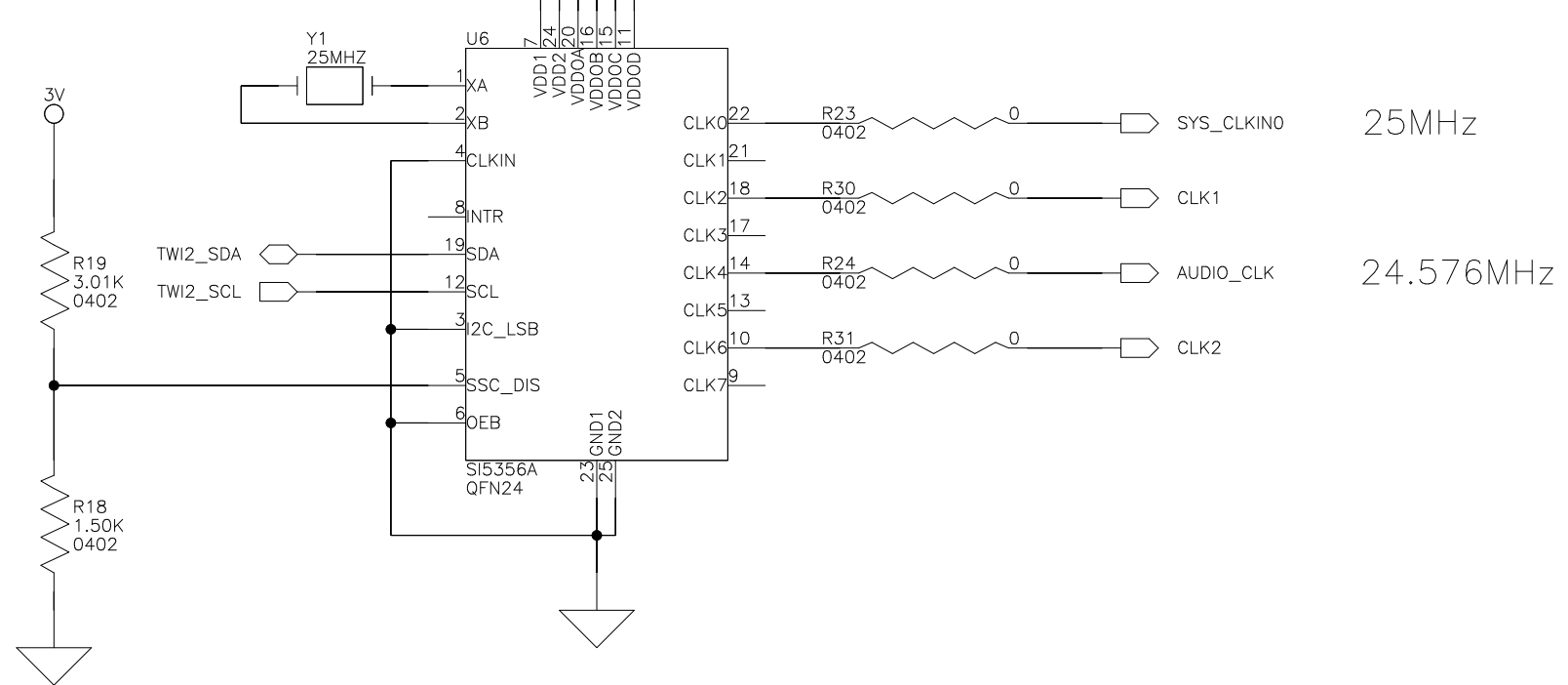
3

OPTIONAL DSP CRYSTALS

R23 should be depopulated when using the optional circuit




twi Address 1110000x
x is the R/W bit. Read - 1, Write - 0



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4



2 Elizabeth Drive
Chelmsford, MA 01824
PH: 1-800-ANALOGD

Title			ADSP-21569 SoM Clocks/Boot Mode		
Size C	Board No.				Rev B
Date				11/23/2020	Sheet 7 of 12

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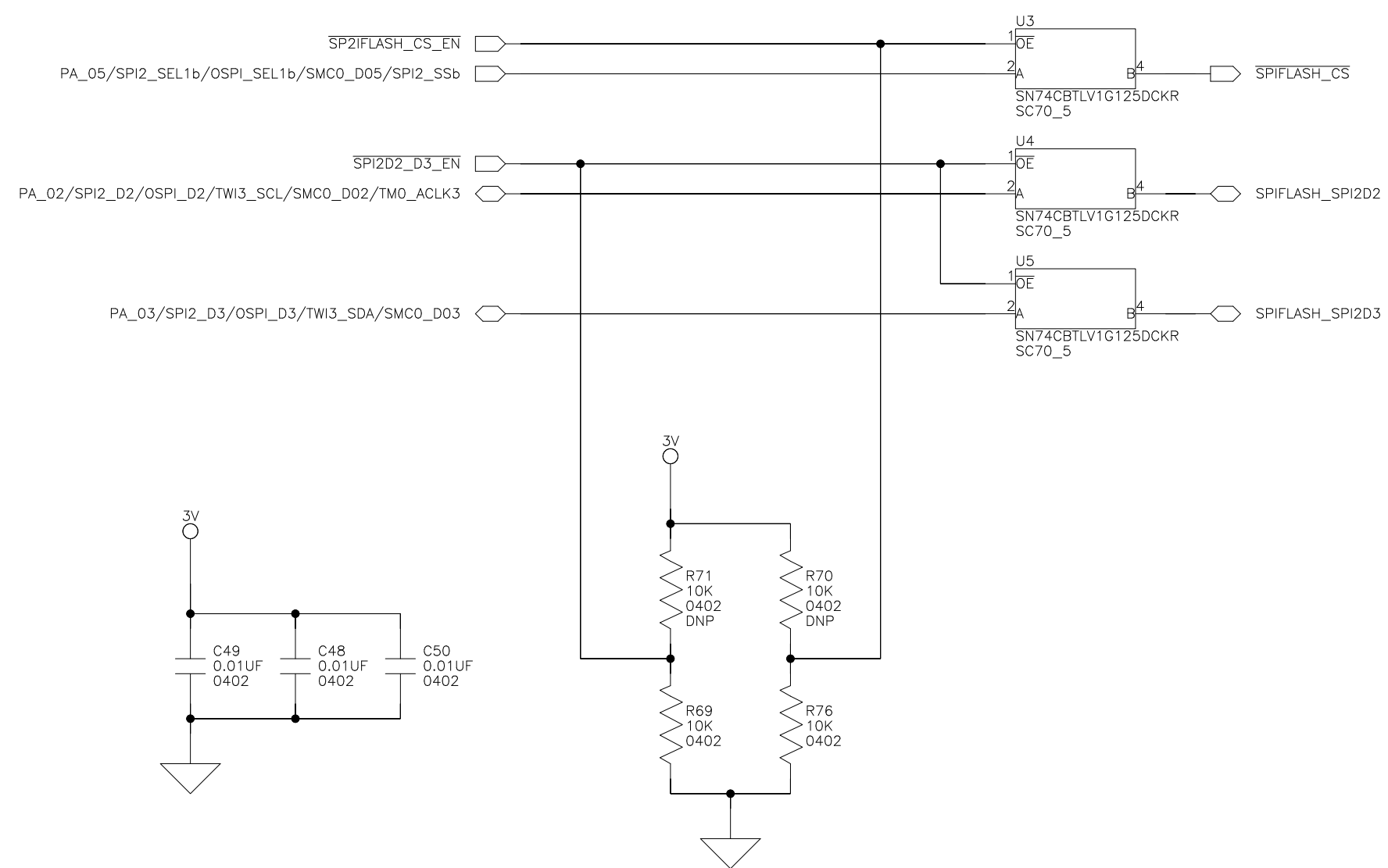
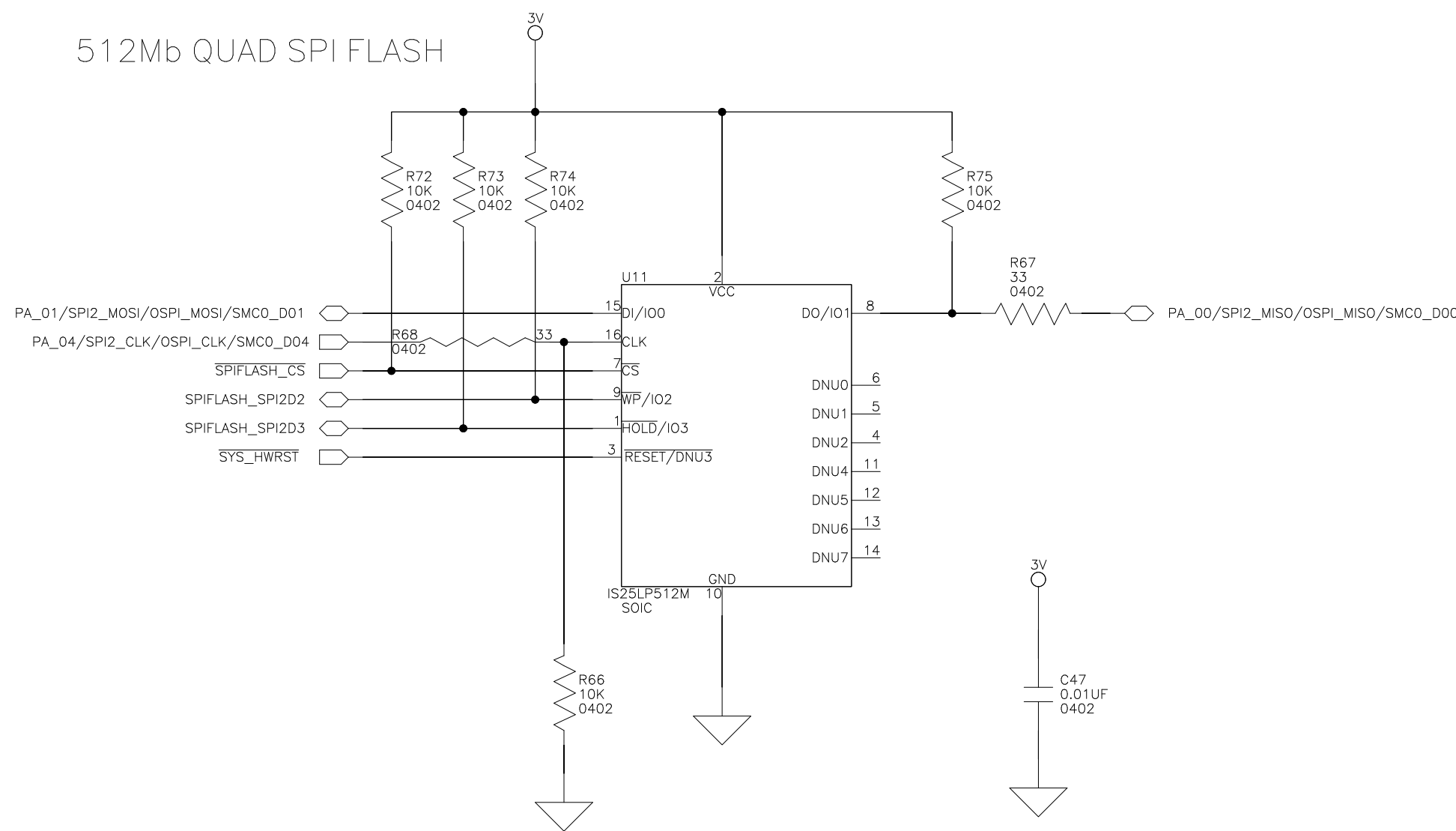
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		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title ADSP-21569 SoM SPI Flash			
Size C	Board No. EV-21569-SOM		Rev B
Date	11/23/2020		Sheet 8 of 12

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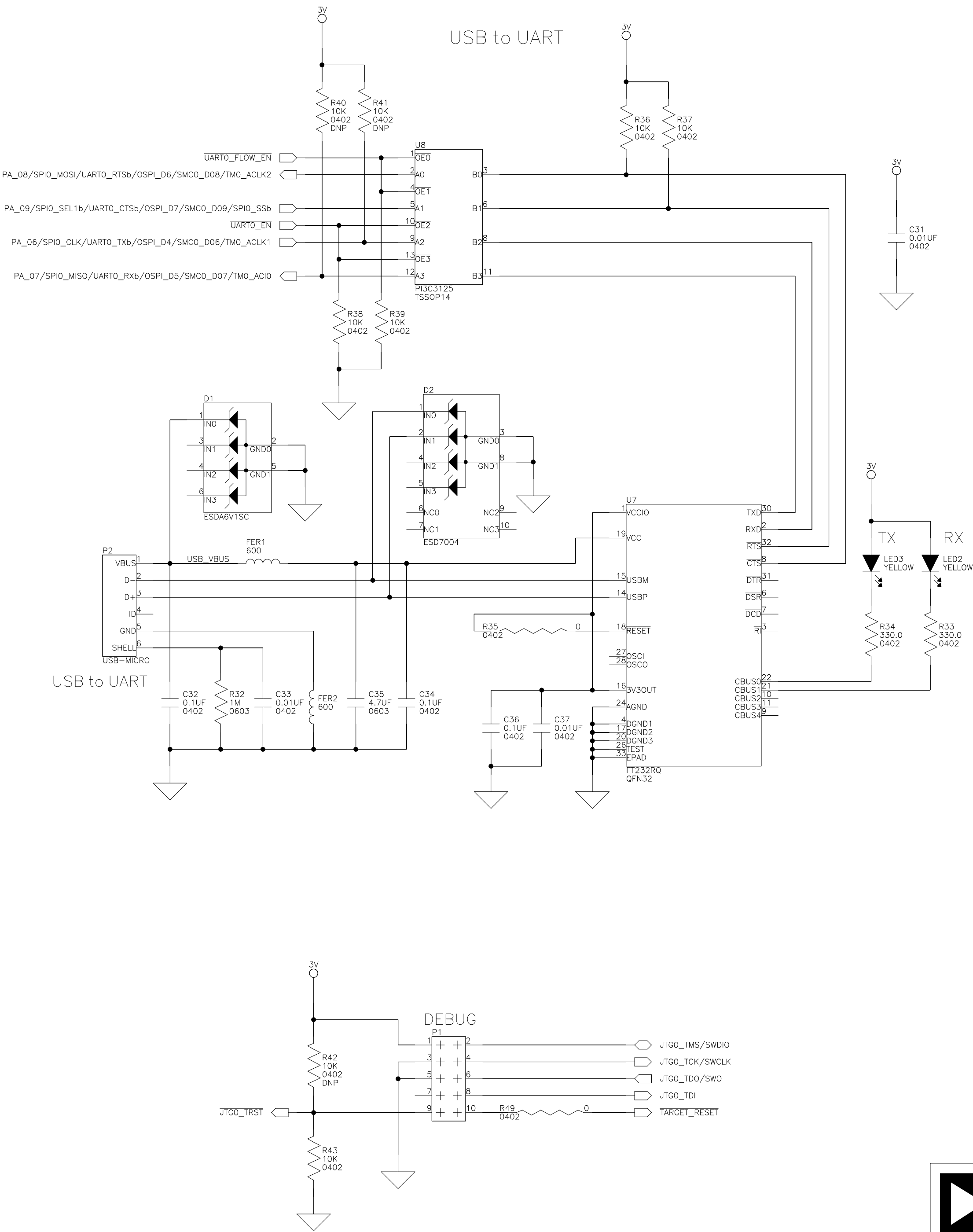
4


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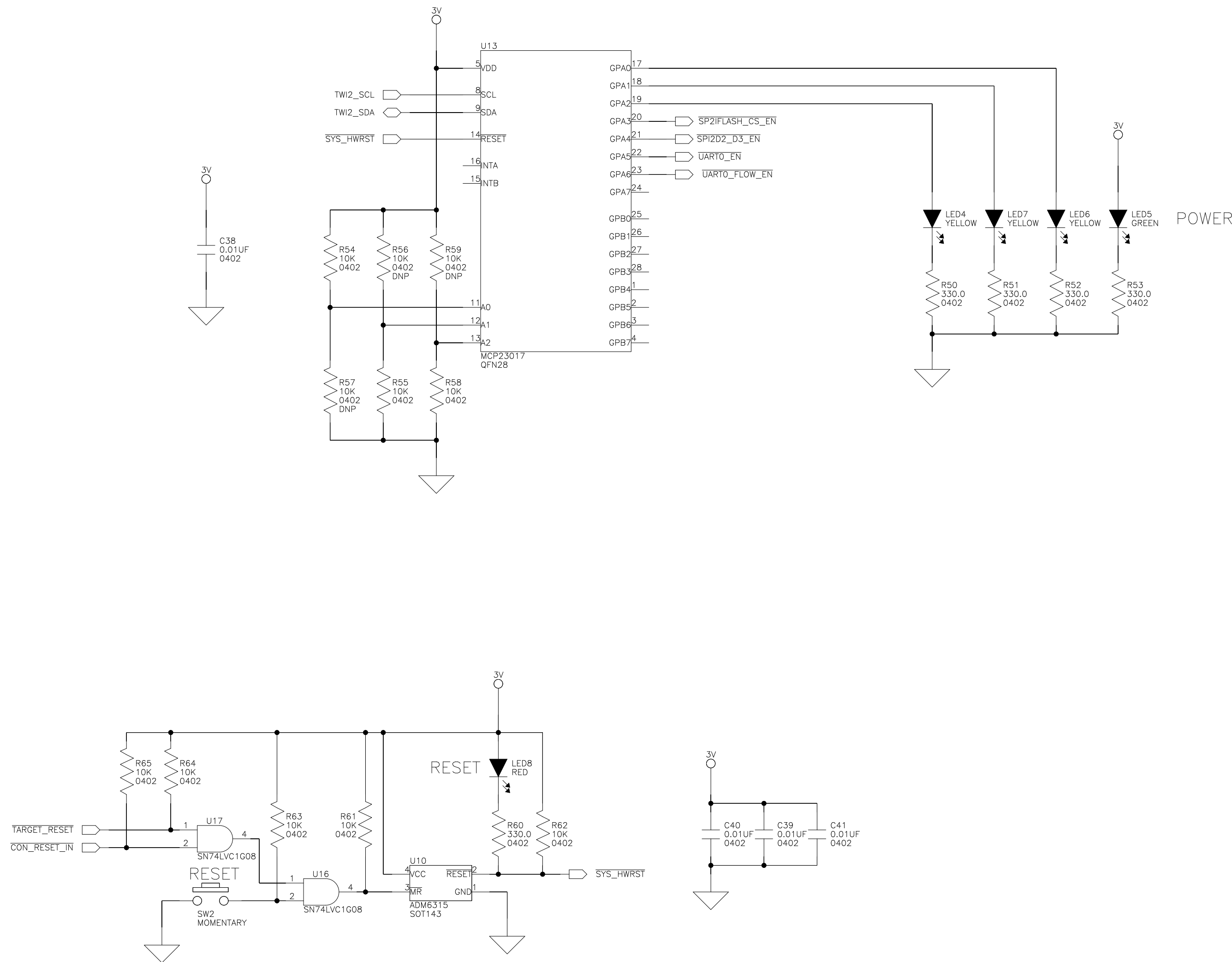
3

4

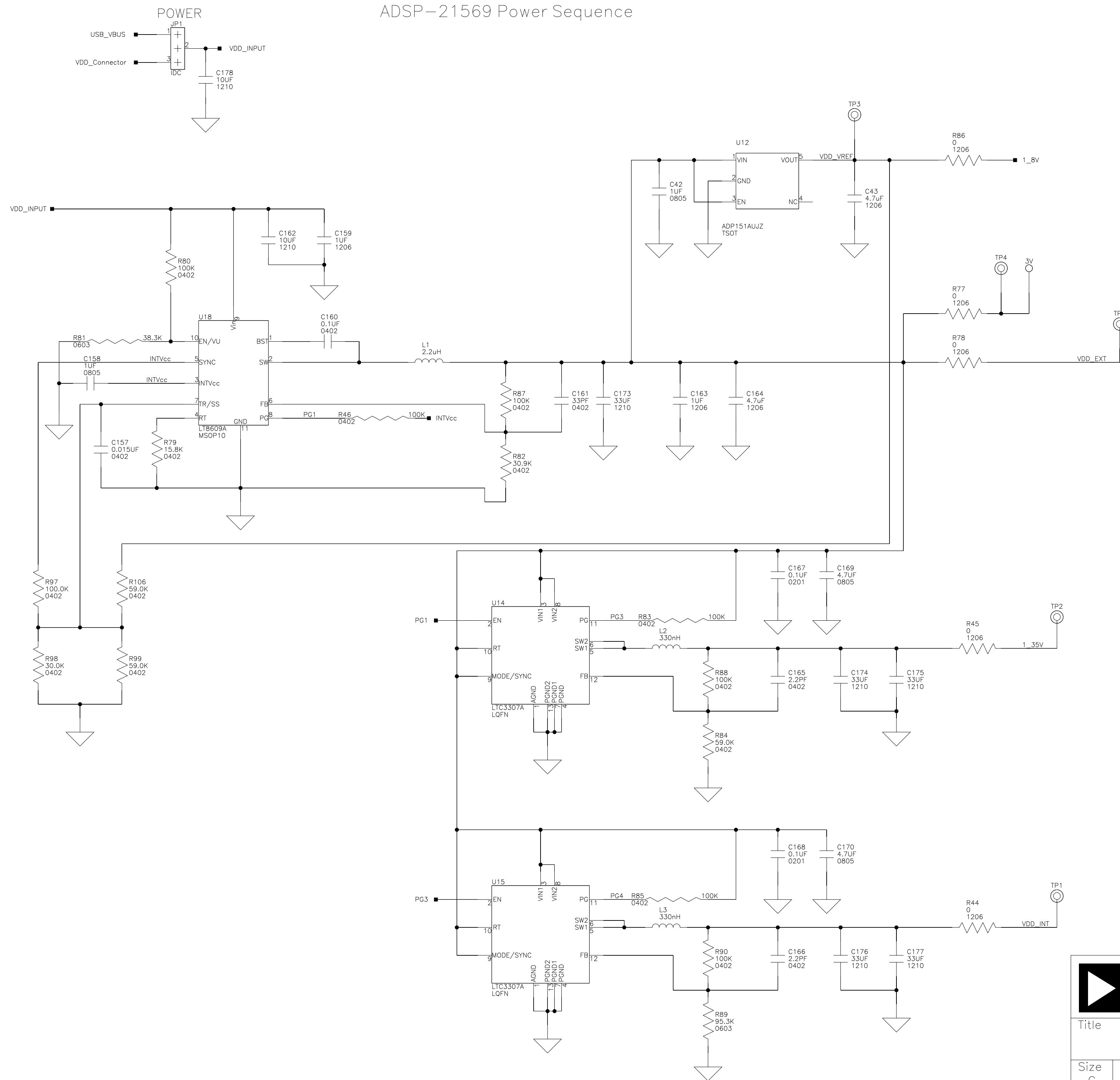


		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title		ADSP-21569 SoM UART/JTAG	
Size C	Board No. EV-21569-SOM		Rev B
Date	11/23/2020		Sheet 9 of 12

TWI Address 0100001x
x is the R/W bit. Read – 1, Write – 0



ADSP-21569 Power Sequence



2 Elizabeth Drive
Chelmsford, MA 01824
PH: 1-800-ANALOGD

Title	ADSP-21569 SoM Power
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Size C	Board No. EV-21569-SOM
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Date	11/23/2020	Sheet	11	of	12
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Rev
B

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